


ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention	Method To Reduce Stacking Fault Nucleation Sites And Reduce Forward Voltage Drift In Bipolar Devices																																																																																																																		
Submission Type :	Utility Patent Filing																																																																																																																		
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Server Response:	<table border="1"> <thead> <tr> <th>Confirmation Code</th> <th>Message</th> </tr> </thead> <tbody> <tr> <td>ISVR1</td> <td>Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application</td> </tr> <tr> <td>ICON1</td> <td>2311</td> </tr> <tr> <td>WUTL2</td> <td>Filename= 5000319-pkda.xml BusinessRule= The utility patent submission does not include at least one declaration.</td> </tr> </tbody> </table>			Confirmation Code	Message	ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application	ICON1	2311	WUTL2	Filename= 5000319-pkda.xml BusinessRule= The utility patent submission does not include at least one declaration.																																																																																																								
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First Named Applicant:	Joseph Sumakeris																																																																																																																		
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